

ABSTRACT OF THE DISCLOSURE

An interface circuit having a plug and play function executes a predetermined bus reset sequence in response to a bus reset. During execution, the circuit analyzes relevant data to determine whether an error has occurred. If so, it deletes the information and re-issues the bus reset to re-execute the bus reset sequence. If an error has not occurred, the interface notifies the host controller and supplies it with the relevant data. Because the host controller is notified only if the reset sequence is executed successfully, the application software and the device driver do not process errors. Thus the interface prevents a detected abnormality from affecting the network.